



**Department of Electronics and Communication Engineering
Indian Institute of Information Technology Allahabad
Allahabad-211012 (UP) India**

Advt. No. IIITA/ECE/SS/PRJ-CRG-31/01/23

Dated - 31/01/2023

Advertisement for the Post of Junior Research Fellow (JRF) (On Contract)

Applications are invited for the post of Junior Research Fellow (JRF) (on contract) in a research project titled “Spin induced properties of p-block elements based Van-der Waals heterostructures for prospective spintronics applications: An ab-initio approach” with **Dr. Sanjai Singh as PI**, in reference to the approval letter for file number CRG/2022/008117 dated: 19th January 2023, by Science and Engineering Research Board (SERB), Department of Science & Technology, New Delhi-110070. The duly completed application in the prescribed format along with copies of supporting documents must reach to **Dr. Sanjai Singh, Department of Electronics & Communication Engineering, Room No. 5147, CC-3 Building, Indian Institute of Information Technology - Allahabad, Deoghat, Jhalwa, Prayagraj, Uttar Pradesh 211015 on or before 21/02/2023** with the subject as “Application for the post of Junior Research Fellow (JRF) (on contract) in the SERB Research Project- File No. CRG/2022/008117”. A soft copy of the application should also be sent to ssingh@iiita.ac.in (PI of the project).

Title of the Project: “Spin induced properties of p-block elements based Van-der Waals heterostructures for prospective spintronics applications: An ab-initio approach”

Name of Position: Junior Research Fellow (JRF)

Number of Positions: 01 (one)

Project Duration: Three (03) Years

Essential Qualifications - M.E/M.Tech/M.Sc. in the subjects related to electronics and Physics.

Desirable Qualifications- Qualified in GATE/ NET

Fellowship: 31,000/- PM (Fixed) for two years and 35,000/- PM (Fixed) for the third year.

HRA: As per DST norms (If applicable).

Note(s)

1. The candidate should be able to travel across various Institutes/Research Centers for the project related work whenever needed.
2. The applicant will be responsible for the authenticity of the information, other documents, and photographs submitted.
3. The institute reserves the right to accept/reject the application at any time, and/or consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed at any point of time for exceptional candidates.
4. Mere, possessing the prescribed qualification does not ensure that the candidate will be called for an Interview. The candidates will be shortlisted on the basis of merit and need of the project.
5. Shortlisted candidates will be informed by e-mail about the interview date. So, the candidate must provide valid e-mail ID(s) in their application.
6. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
7. Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
8. No TA/DA will be paid for appearing in the interview.
9. The first and foremost responsibility of the candidate is to fulfill the jobs’ requirements to complete the above mentioned project in timely manner.
10. The association between PI and the candidate can be discontinued at any time by giving one month’s notice in writing either by the PI of the project to the candidate, or by candidate to the PI of the project.
11. Candidate selected for this position may also apply for the regular Ph.D. program in ECE Department, provided he/she satisfies the eligibility criterion followed by the selection process laid down by the institute (IITA).
12. Hostel accommodation may be provided as per institute norms

Dr. Sanjai Singh
Principal Investigator (PI)
ECE Department
IIT-Allahabad-211012 (U.P.)-India



Department of Electronics and Communication Engineering
Indian Institute of Information Technology Allahabad
Allahabad-211012 (UP), India

Advt. No. IIITA/ECE/SS/PRJ-CRG-01/23

For office use only
Application No.....

APPLICATION FORM

Dated: 31/01/2023

Research Project Entitled- "Spin induced properties of p-block elements based Van-der Waals heterostructures for prospective spintronics applications: An ab-initio approach"

File No. : CRG/2022/008117
Funding Agency: Science and Engineering Research Board (SERB), New Delhi
Name of P.I.: Dr. Sanjai Singh
Name of position: Junior Research Fellow (on contract)
Department/Section: Department of Electronics and Communication Engineering

1. Name of the Candidate (In Capital):
2. Sex (Male/Female):
3. Marital Status (Married/Unmarried):
4. Date of Birth:
5. Age (as on last date of application): _____ Years _____ Months _____ Days
6. Father's/Husband's Name:
7. Present/Contact Address:
8. Permanent Address:
9. Mobile No:
10. E-mail:
11. Nationality:
12. Category Gen/EWS/SC/ST/OBC/PH:

13. GATE/NET:

Branch	Year	Valid up to	Percentile (& Score)	All India Rank

14. Educational Qualification (10th onward):

Qualification	Subject/Discipline	Board/Institute/Univ.	Year of passing	% of marks	Division
10th					
12th					
Diploma, if any					
B.E./B.Tech/M.Sc.					
M.E./M.Tech					

15. Summary of M.E/M.Tech Thesis (Maximum 150 words): Enclose a separate sheet

16. Research Publications (Maximum Best 5): Enclose a separate sheet

17. Details of Experience in chronological order. Enclose a separate sheet

18. Any other information: Enclose a separate sheet

19. Indicate whether you propose to register for higher degree at IIITA, Allahabad: Yes/No
(Name of higher degree is Ph.D.)

DECLARATION

I hereby declare that all the statements made in this application are true and complete, and nothing has been concealed/distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place:

Signature of the Applicant

Date:

Checklist-

1. Self-attested copy of documents
2. Proof of Date of Birth
3. Mark sheets & certificates of all the examinations passed
4. Category Certificate, if applicable
5. GATE/NET score card, if passed
6. Experience certificate, if any